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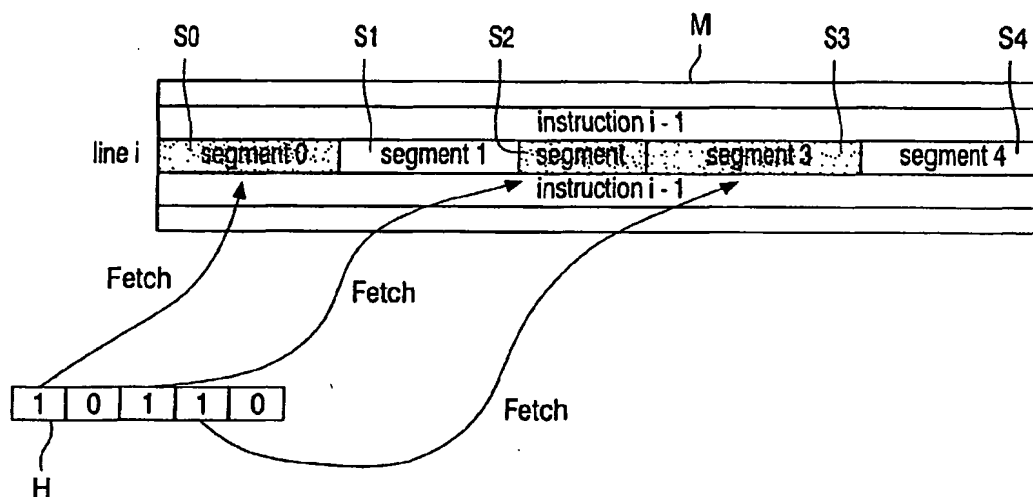
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(54) Title: PROCESSOR AND METHOD FOR PROCESSING VLIW INSTRUCTIONS



(57) **Abstract:** Described are a processor device and a method for processing instructions, in particular VLIW instructions, wherein instruction words are stored in a memory means, each instruction word consisting of segments, the instruction words are fetched from the memory means, and instructions are executed in accordance with instruction words fetched from the fetching means. The particularity of the invention is in that essentially those segments of an instruction word are fetched only which contain relevant information.